

**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of	Docket No: Q77403
Kenji SERA, et al.	Group Art Unit: 2813
Appln. No.: 10/657,196	Examiner: Thanhha S. PHAM
Confirmation No.: 1621	Patent No.: 7,224,224
Filed: September 9, 2003	Issue Date: May 29, 2007
For: THIN FILM SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD	

**REQUEST FOR CERTIFICATE OF CORRECTION**

**ATTN: Certificate of Correction Branch**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to the provisions of 37 C.F.R. § 1.322, please enter the attached Certificate of Correction.

Since the errors noted are believed to be the fault of the Patent and Trademark Office, we are not enclosing the \$100.00 Certificate of Correction fee. If it is found to be to the contrary, please charge our Deposit Account No. 19-4880.

In view of the foregoing, issuance of the Certificate of Correction is respectfully requested.

Respectfully submitted,

  
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WASHINGTON OFFICE

**23373**

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Date: August 15, 2008

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO: 7,224,224  
DATED: May 29, 2007  
INVENTOR(S): Kenji SERA  
Hiroshi TSUCHI

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 5, line 14. Delete [comprises] and insert -- comprise --

Claim 12, col. 29, line 34. After "a" insert -- ninth --

Claim 14, col. 30, line 5. After "low" insert -- absolute value of the seventh threshold value and comprising a control --

Claim 24, col. 31, line 48. Delete "comprised" and insert -- comprises --

Claim 25, col. 31, line 56. Before "said" insert -- having --

MAILING ADDRESS OF SENDER:

**SUGHRUE MION, PLLC**

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PATENT NO. 7,224,224

No. of additional copies

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